

BUFFER CIRCUIT DEVICE SUPPLYING A COMMON MODE VOLTAGE  
APPLICABLE TO A NEXT-STAGE CIRCUIT RECEIVING OUTPUT  
SIGNALS OF THE BUFFER CIRCUIT DEVICE

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ABSTRACT OF THE DISCLOSURE

10           A buffer circuit device, which receives differential  
input signals and outputs differential output signals,  
has a buffer circuit, a common mode level generator  
circuit, a common mode voltage detection circuit, and a  
bias voltage adjusting circuit. The buffer circuit  
15 receives the input signals and outputs the output  
signals, and the common mode level generator circuit  
outputs a specific level of a common mode voltage of the  
output signals to be output from the buffer circuit. The  
common mode voltage detection circuit detects a common  
20 mode voltage of specific signals, and the bias voltage  
adjusting circuit adjusts a bias voltage to be supplied  
to the buffer circuit by comparing an output signal of  
the common mode level generator circuit with an output  
signal of the common mode voltage detection circuit.